

ABSTRACT OF THE DISCLOSURE

Provided are a flip flop and a shift resistor whose layout areas are small. A data input terminal is connected with a first terminal of a first switching element, and a second terminal of the first switching element is connected with an input terminal of a first inverter element. Further, an output of the first inverter element is inputted to a first terminal of a second switching element, a second terminal of the second switching element is connected with an input terminal of a second inverter element, and an output terminal of the second inverter element is a data output terminal.